Reference list

	1	clock and data recovery unit
	2	data input
5	3	data output
	4	reference clock input
	5	clock output terminal
	6	loss indicating output terminal
	7	lock indicating output terminal
10	8	delay locked loop
	9	phase interpolation unit
	10	oversampling unit
	11	input buffer
	12	serial-to-parallel-conversion unit
15	13	digital control unit
	13a	register
	13b	binary phase detector
	13c	loop filter
	13d	data recognition unit
20	13e	loss of transition detection unit
	13f	lock detection unit
	14	control line
	15	internal bus
	16	line
25	17	internal clock line